



➤ **KAB-ADAPT-LVDS**

Document Revision 1.1



Contents

1. USER INFORMATION.....	1
1.1 About This Document	1
1.2 Copyright Notice	1
1.3 Trademarks	1
1.4 Standards	1
1.5 Warranty.....	2
1.6 Technical Support.....	2
2. INTRODUCTION	3
2.1 KAB-ADAPT-LVDS.....	3
2.2 JILI Family	3
2.3 JILI30 Family.....	3
2.4 JILI40 Family.....	3
3. SPECIFICATIONS.....	3
3.1 Mechanical Specifications	3
3.1.1 Cable lengths	3
3.1.2 Module Dimensions (PCB)	3
3.1.3 Weight	3
3.2 Electrical Specifications	4
3.2.1 Supply Voltage.....	4
3.2.2 Supply Voltage.....	4
3.2.3 Supply Current.....	4
3.2.4 Panel Current.....	4
4. ENVIRONMENTAL SPECIFICATIONS.....	4
4.1 Temperature.....	4
4.2 Humidity.....	4
5. CONNECTOR.....	5
5.1 JILI Connector (X1).....	5
5.2 JILI30 Connector (X2)	6
5.3 JILI40 Connector (X3)	7
5.4 Backlight Connector (X5)	7
6. CONFIGURATION	8
6.1 Panel Power (J1)	8
6.2 Backlight Power (J1).....	8
6.3 Backlight Power Enable (J2)	8
APPENDIX A: REVISION HISTORY	9

1. User Information

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Asia	Europe	North/South America
Kontron Asia Inc.	Kontron Embedded Modules GmbH	Kontron America
4F, No.415, Ti-Ding Blvd., NeiHu District, Taipei 114, Taiwan	Brunnwiesenstr. 16 94469 Deggendorf – Germany	14118 Stowe Drive Poway, CA 92064-7147
Tel: +886 2 2799 2789	Tel: +49 (0) 991-37024-0	Tel: +1 (888) 294 4558
Fax: + 886 2 2799 7399	Fax: +49 (0) 991-37024-333	Fax: +1 (858) 677 0898
mailto:sales@kontron.com.tw	mailto:sales-kem@kontron.com	mailto:sales@us-kontron.com

2. Introduction

2.1 KAB-ADAPT-LVDS

KAB-ADAPT-LVDS is an evaluation board (only for development) vor VGA-, SVGA-, XGA-, SXGA-LVDS TFT flat-panels to Kontron JILI interface.

2.2 JILI Family

JILI (**J**umpte**c** **I**ntelligent **L**V**D**S **I**nterface) is a Kontron hardware standard that interfaces the host computer system via LVDS to flat-panel displays. The target displays are usually medium to high resolution TFT displays with parallel (CMOS, non-LVDS) interfaces. A detailed description of the JILI interface standard also is available in a separate document JILIM???.PDF. The three question marks represent the document's revision number. You can download the document from the Kontron website, or contact your local Kontron technical support to receive it.

2.3 JILI30 Family

JILI30 (**J**umpte**c** **I**ntelligent **L**V**D**S **I**nterface) is a Kontron hardware standard that interfaces the host computer system via LVDS to flat-panel displays. The target displays are usually medium to high resolution TFT displays with parallel (CMOS, non-LVDS) interfaces.

2.4 JILI40 Family

JILI40 (**J**umpte**c** **I**ntelligent **L**V**D**S **I**nterface) is a Kontron hardware standard that interfaces the host computer system via LVDS to flat-panel displays. The target displays are usually medium to high resolution TFT displays with parallel (CMOS, non-LVDS) interfaces.

3. Specifications

3.1 Mechanical Specifications

3.1.1 Cable Length

- 500 mm \pm 10 mm

3.1.2 Module Dimensions (PCB)

- 50x 80 mm, without cable
- Height approx. 12 mm without cable

3.1.3 Weight

- 50 g

3.2 Electrical Specifications

3.2.1 Supply Voltage

- 5 V DC $\pm 5\%$, 12V DC $\pm 5\%$

3.2.2 Supply Voltage

- 500 mV peak to peak 0 – 20 MHz

3.2.3 Supply Current

- See datasheet from panel

3.2.4 Panel Current

- Maximum current: 600 mA* by 3.3 V panel VCC
- Maximum current: 2 A* by 5 V panel VCC
- Maximum current: 1.5 A* by 12 V panel VCC
- Maximum current: 1 A* by 5 V backlight VCC
- Maximum current: 1 A* by 12 V backlight VCC
- * The max. current on the standard JILI FFC cable is 2 A (5 V) and 1,5 A (12 V). If the panel or backlight power exceeds these values an external power supply should be implemented.

4. Environmental Specifications

4.1 Temperature

- Operating (with appropriate airflow):
 - Maximum operating temperature: 0 to +60 °C (*)
- Non operating: -10 to +85 °C

Note:

(*) The maximum operating temperature is the maximum measurable temperature on any spot on a module's surface. You must maintain the temperature according to the above specification.

4.2 Humidity

- Operating: 10% to 90% (non condensing)
- Non operating: 5% to 95% (non condensing)

5. Connector

5.1 JILI Connector (X1)

Flatfoil connector, right angle, bottom contact, 0.5 mm pitch, 40 contacts.

Pin	Signal	Function	Pin	Signal	Function
1	DETECT	Not connected	2	FTX0-	First Transmitter Signal
3	FTX0+	First Transmitter Signal	4	ENAVDD	Panel Power Enable
5	FTX1-	First Transmitter Signal	6	FTX1+	First Transmitter Signal
7	NC	Not connected	8	FTX2-	First Transmitter Signal
9	FTX2+	First Transmitter Signal	10	GND	Power Ground
11	FTXC-	First Transmitter Clock	12	FTXC+	First Transmitter Clock
13	GND	Power Ground	14	FTX3-	First Transmitter Signal
15	FTX3+	First Transmitter Signal	16	DDCDAT	I2C Data
17	STX0-	Second Transmitter Signal	18	STX0+	Second Transmitter Signal
19	DDCCLK	I2C Clock	20	STX1-	Second Transmitter Signal
21	STX1+	Second Transmitter Signal	22	GND	Power Ground
23	STX2-	Second Transmitter Signal	24	STX2+	Second Transmitter Signal
25	GND	Power Ground	26	STXC-	Second Transmitter Clock
27	STXC+	Second Transmitter Clock	28	GND	Power Ground
29	STX3-	Second Transmitter Signal	30	STX3+	Second Transmitter Signal
31	+5V	+5V Power	32	+5V	+5V Power
33	+5V	+5V Power	34	+5V	+5V Power
35	/BLON	Backlight Power Enable	36	GND	Power Ground
37	GND	Power Ground	38	+12V	+12V Power
39	+12V	+12V Power	40	+12V	+12V Power

5.2 JILI30 Connector (X2)

Connector single row, 1.00 mm pitch, 30 contacts (JAE, FI-X30S-HF or equivalent).

Pin	Signal	Function
1	FTX0-	First Transmitter Signal
2	FTX0+	First Transmitter Signal
3	FTX1-	First Transmitter Signal
4	FTX1+	First Transmitter Signal
5	FTX2-	First Transmitter Signal
6	FTX2+	First Transmitter Signal
7	GND	Power Ground
8	FTXC-	First Transmitter Clock
9	FTXC+	First Transmitter Clock
10	FTX3-	First Transmitter Signal
11	FTX3+	First Transmitter Signal
12	STX0-	Second Transmitter Signal
13	STX0+	Second Transmitter Signal
14	GND	Power Ground
15	STX1-	Second Transmitter Signal
16	STX0+	Second Transmitter Signal
17	GND	Power Ground
18	STX2-	Second Transmitter Signal
19	STX2+	Second Transmitter Signal
20	STXC-	Second Transmitter Clock
21	STXC+	Second Transmitter Clock
22	STX3-	Second Transmitter Signal
23	STX3+	Second Transmitter Signal
24	GND	Power Ground
25	I2CDAT	Not connected
26	ENAVDD	Panel Power Enable
27	I2CCLK	Not connected
28	PANEL_VCC	Panel VCC
29	PANEL_VCC	Panel VCC
30	PANEL_VCC	Panel VCC

5.3 JILI40 Connector (X3)

Connector double row, 1.00 mm pitch, 40 contacts.

Pin	Signal	Function	Pin	Signal	Function
1	BACK_VCC	Backlight VCC	2	BACK_VCC	Backlight VCC
3	BRIGHT	Brightness (0-5V)	4	STX3+	Second Transmitter Signal
5	STX3-	Second Transmitter Signal	6	+12V	Not connected
7	STXC+	Second Transmitter Clock	8	STXC-	Second Transmitter Clock
9	+12V	Not connected	10	STX2+	Second Transmitter Signal
11	STX2-	Second Transmitter Signal	12	+12V	Not connected
13	STX1+	Second Transmitter Signal	14	STX1-	Second Transmitter Signal
15	+12V	Not connected	16	STX0+	Second Transmitter Signal
17	STX0-	Second Transmitter Signal	18	+12V	Not connected
19	FTX3+	First Transmitter Signal	20	FTX3-	First Transmitter Signal
21	GND	Power Ground	22	GND	Power Ground
23	I2CCLK	Not connected	24	I2CDAT	Not connected
25	GND	Power Ground	26	FTXC+	First Transmitter Clock
27	FTXC-	First Transmitter Clock	28	GND	Power Ground
29	FTX2+	First Transmitter Signal	30	FTX2-	First Transmitter Signal
31	GND	Power Ground	32	FTX1+	First Transmitter Signal
33	FTX1-	First Transmitter Signal	34	GND	Power Ground
35	FTX0+	First Transmitter Signal	36	FTX0-	First Transmitter Signal
37	GND	Power Ground	38	GND	Power Ground
39	PANEL_VCC	Panel VCC	40	PANEL_VCC	Panel VCC

5.4 Backlight Connector (X5)

Connector single row, 1.25 mm pitch, 7 contacts (Molex, 53261-0771 or equivalent).

Pin	Signal	Function
1	NC	Not connected
2	BRIGHT	Brightness (0-5V)
3	GND	Power Ground
4	BACK_VCC	BACK_VCC
5	BACK_VCC	BACK_VCC
6	GND	Power Ground
7	ENABKL	Backlight Power Enable

6. Configuration

6.1 Panel Power (J1)

Caution! Only one configuration for backlight and flatpanel is allowed, otherwise the board is permanently damaged.

Pin	Function
5-6	Panel +12V
7-8	Panel +5V
9-10	Panel +3.3V

6.2 Backlight Power (J1)

Caution! Only one configuration for backlight and flatpanel is allowed, otherwise the board is permanently damaged.

Pin	Function
1-2	Backlight +5V
3-4	Backlight +12V

6.3 Backlight Power Enable (J2)

Pin	Function
1-2	Backlight Power Enable (low active)
2-3	Backlight Power Enable (high active)

APPENDIX A: REVISION HISTORY

Revision	Date	Edited by	Changes
1.0	06/11/2007	S. Leuchtenberger	First revision
1.1	07/22/2010	HAM	Correction of backlight power jumper settings